



एम एस एम ई - टूल रूम, हैदराबाद
MSME-TOOL ROOM, HYDERABAD
केंद्रीय उपकरण अभिकल्प संस्थान
Central Institute of Tool Design



CITD/Trg./Diploma/Field Practice/24

Date - 17.01.2025

NOTICE

All the trainees of DECE & DARE - 2022 Batch (VI Semester) are hereby directed note the following.

“The Field Practice Phase-I {PCB Design & Fabrication}” will be conducted as per the attached schedule.

**Deputy Director
(Training/APCU)**

Copy to:

1. Class Coordinators by e-circulation for coordination and recording attendance.
2. Trainees by e-circulation.
3. All Notice Boards.
4. Examination Cell for record.
5. System Admin-To upload on website.

Week	SI NO	Date	Day	Timings	Topic	Lab	Theory
week-1	1	27-01-2025	Monday	3Pm - 5pm	Introduction & Basics of PCB	---	Batch-1&2
	2	28-01-2025	Tuesday	3Pm - 5pm			
	3	29-01-2025	Wednesday	3Pm - 5pm			
	4	30-01-2025	Thursday	3Pm - 5pm	Ki-cad software installation & work flow of kicad	---	
	5	31-01-2025	Friday	3Pm - 5pm	Introduction to schematic design and It's design tools	---	
	6	01-02-2025	Saturday	3Pm - 5pm	Library management and circuit design	---	
	7	02-02-2025	Sunday	3Pm - 5pm			
week-2	8	03-02-2025	Monday	3Pm - 5pm	Schematic design and ERC	Batch - 2	Batch - 1
	9	04-02-2025	Tuesday	3Pm - 5pm		Batch - 1	Batch - 2
	10	05-02-2025	Wednesday	3Pm - 5pm	Foot print assignment and symbol creation	Batch - 2	Batch - 1
	11	06-02-2025	Thursday	3Pm - 5pm		Batch - 1	Batch - 2
	12	07-02-2025	Friday	3Pm - 5pm		foot print creation and simulation	Batch - 2
	13	08-02-2025	Saturday	3Pm - 5pm			
	14	09-02-2025	Sunday	3Pm - 5pm			
Week - 3	15	10-02-2025	Monday	3Pm - 5pm	foot print creation and simulation	Batch - 1	Batch - 2
	16	11-02-2025	Tuesday	3Pm - 5pm	PCB layout design	Batch - 2	Batch - 1
	17	12-02-2025	Wednesday	3Pm - 5pm		Batch - 1	Batch - 2
	18	13-02-2025	Thursday	3Pm - 5pm		Batch - 2	Batch - 1
	19	14-02-2025	Friday	3Pm - 5pm		Batch - 1	Batch - 2
	20	15-02-2025	Saturday	3Pm - 5pm	DRC & PCB output file generation	Batch - 2	Batch - 1
	21	16-02-2025	Sunday	3Pm - 5pm			
Week - 4	22	17-02-2025	Monday	3Pm - 5pm	DRC & PCB output file generation	Batch - 1	Batch - 2
	23	18-02-2025	Tuesday	3Pm - 5pm	circuit validation and design rules	Batch - 2	Batch - 1
	24	19-02-2025	Wednesday	3Pm - 5pm	circuit validation and design rules	Batch - 1	Batch - 2
	25	20-02-2025	Thursday	3Pm - 5pm	PCB fabrication process	---	Batch-1&2
	26	21-02-2025	Friday	3Pm - 5pm			
	27	22-02-2025	Saturday	3Pm - 5pm	Standard for pcb design and fabrication	---	
	28	23-02-2025	Sunday	3Pm - 5pm			

* Batch 1 - DECE

* Batch 2 - DARE