

**PROSPECTUS FOR
POST GRADUATE DIPLOMA in VLSI –
EMBEDDED SYSTEMS 2009.**



CENTRAL INSTITUTE OF TOOL DESIGN
Govt. of India Society, Ministry of SSI
Balanagar, Hyderabad-500 037, A.P. India



ISO 9001 : 2000
In Collaboration With



ISO 9001:2000

ELECTRONICS CORPORATION OF INDIA LIMITED
A Govt of India Undertaking
Hyderabad – 500 062, A.P., India

ABOUT CITD & ECIL

1 Aims & Objectives 1

POST GRADUATE DIPLOMA IN VLSI – EMBEDDED SYSTEMS 2

2.1 Medium of Instruction 2

2.2 Commencement of Course and Working hours 2

2.3 Methodology of imparting training

2.4 Evaluation and Examination

2.5 Minimum requirements for Award of Certificate 2

2.6 Issue of certificates & memorandum of marks 2

2.7 Detention / Termination 3

ADMISSION TO LONG TERM TRAINING COURSES 3

3 Applications 4

4. INSTRUCTIONS FOR FILLING UP THE APPLICATION FORMS 4

5. SYLLABUS FOR ENTRANCE EXAMINATION 5

6. MODE OF SELECTION 5

7. DOCUMENTS TO BE HANDED OVER AT THE TIME OF JOINING 6

8. JURIDICTION 6

9. GENERAL TERMS & CONDITIONS FOR TRAINEES ADMITTED TO LONG TERM COURSES 6

Last date for receipt of duly filled-in Application (upto 5.00 p.m. at CITD) : **25-07-2009**

Last date for receipt of duly filled-in Application (upto 5.00 p.m. at CITD) with late fee : **27-07-2009**

Date of Entrance Test : **09-08-2009 (Sunday)**

Announcement of results : **12-08-2009**

Date of 1st counseling : **17-08-2009**

Date of 2nd counseling : **19-08-2009**

Commencing of course : **26-08-2009**

Venue : **CENTRAL INSTITUTE OF TOOL DESIGN**

(A Govt. of India Society, Ministry of SSI)

Balanagar, Hyderabad-500 037, Andhra Pradesh

Phone Nos.: 23772749, 23776178, Fax No.:040-23772658

E-mail: citdcadcam@citdindia.org

Internet: www.citdindia.org .

N.B. - In case of non-receipt of Hall Ticket on or before 08-08-2009, the candidate is advised to contact CITD with a pass port size photograph for obtaining duplicate hall ticket 2 hours before the examination

- o. The rules are liable to change from time to time depending upon exigencies of training.
- p. The Institute reserves the right to terminate the trainee at any time at its discretion for misconduct/indiscipline/irregular attendance, breach of rules etc. In such an event, the course fee paid by the candidates shall not be refunded.
- q. The trainees should deposit caution money of Rs. 1,000/- which will be refunded at the conclusion of the course and not before under any circumstance.

The trainees should give an undertaking to abide by the terms & conditions supplied at the time of selection.

1. ABOUT CITD & ECIL

Aims and Objectives :

CITD a pioneering organization in India offering technical training at Post Graduation level in Tool Design and CAD/CAM joined hands with Electronics Corporation of India Ltd. (ECIT) a giant organization in Electronics and training offers the following Diploma programmes. The Government of India established the CITD in the Year 1968 with the assistance of UNDP and ILO as an executing agency.

ECIL is a Government of India Undertaking under the Dept of Atomic Energy established in the year 1967. ECIL's thrust areas are Information Technology, Control & Automation, Communication and Strategic Electronics.

ECIL-ECIT is the IT Education wing of ECIL engaged in imparting high quality IT Education and Training to students, Professionals and Corporate Executives. The Programmes are specially designed with Industry orientation.

The Institute has a well equipped Embedded Systems and VLSI lab facilitating HI-end systems, the state of the art software and hardware such as -Model Sim VHDL and Verilog simulator, Leonardo Spectrum Level 3 Synthesis, Xilinx Trainer Kits, Microcontroller 8051Kits, AVR Microcontroller kit, Keil IDE Tools, Zylog Analogic Controllers and Dynamic C, Xilinx Foundation Series FPGA design station, Linux OS, RTOS : Vx Works/RT Linux.

The Institute has a well equipped Tool room with sophisticated systems such as CNC 5-axis & 4-axis Machining centers, CNC Lathes, CNC EDM machines, CNC wire cut machines etc., supported by Metrology equipment like CNC 3 Co-ordinate measuring machine. The Institute is also equipped with - Calibration centre for calibration of instruments, slip gauges, Automation centre with hydraulic, pneumatic, electronic & PLC laboratories, Library & Documentation centre.

The Institute has a CAD/CAM centre with powerful graphics based Compaq workstations with packages like AutoCAD, I-DEAS, Pro/ENGINEER, CATIA, ANSYS, ADINA, Master CAM, UNIGRAPHICS etc for imparting training and consultancy.

The Institute undertakes design & manufacturing of tooling, machining of precision components using 5-axis machine, consultancy jobs in the area of CAD/CAM, automation, & calibration services in addition to training activities in the area of CAD/CAM, tool design & automation.

2. POST GRADUATE DIPLOMA IN VLSI – EMBEDDED SYSTEMS

Duration : 6 Months

Qualification : Engineering Degree in Computers / Electronics & communications / Electrical / Electronics & Instrumentation / M .Sc (Electronics).

No. of seats : 40 seats through entrance test.

Course fee : Rs. **45,000/-** for candidates selected through entrance test.

Fee can be paid in two installments. Rs. 25,000/- at the time of admission and balance Rs. 20,000/- to be paid before 26.10.2009.

Reservation for S.C/S.T category is considered as per Govt. of India rules.

2.1 Medium of instruction – ENGLISH

2.2 **Commencement of course : Aug 26th 2009** Working hours : 10.00 A.M. to 5.00 P.M. with Sunday as weekly holiday and the working hours are liable for change.

2.3 Methodology of imparting training:

- a) Lectures supported by audio-visual aids.
- b) Film shows on technical subjects.
- c) Exercises for VLSI – EMBEDDED trainees.
- d) Technical literature/handouts on related subjects.

2.4 Evaluation and examination:

The trainees are evaluated in the following manner.
Internal assessment of theory by regular

- d. Hostel facility is available on limited basis and the out station candidates will be allotted hostel if vacancies are available at the time of admissions.
- e. The trainees shall not cause any damage or loss to the property of the Institute, failing which they are liable to bear the loss.
- f. Any information given in the application by the candidate, if, found incorrect after selection, the trainee is liable for such punishment as may be deemed fit including his/her disqualification and/or dismissal from training course.
- g. The trainees are expected to complete the assignments as per schedule of training programmes. Marks will be deducted for late submission of assignments.
- h. The trainees will have to strictly undergo the training as per the schedule and instructions and comply with all the rules of discipline as may be in force from time to time. Copying in examination and any malpractice during the exams will be viewed seriously.
- i. The trainees should complete the full course of training and will not be allowed to discontinue/rejoin the training for any reason.
- j. The trainees are not permitted to join part-time/evening courses of study conducted by any other University/Institute.
- k. The trainees are expected to maintain utmost discipline and good manners inside and outside the campus of the Institute and not get involved in activities which cause blemish to the good image of the Institute.
- l. The trainees have to follow the rules and regulations pertaining to any department of the Institute..
- m. No request for extension of time for submission of proof of pass will be entertained.
- n. The Institute reserves the right to alter, vary, change and/or modify the rules governing the admissions.

The selected candidates have to be present themselves on the dates mentioned below for counseling with all the original certificates & prescribed fees in cash/DD to be considered for admission at **10.00 am at CITD, Balanagar, Hyderabad.**

Date of 1st Counseling: **17-08-2009.**

Date of 2nd Counseling: **19-08-2009.**

Once the selected candidates report for training and the fee paid by the candidates on the counseling day, the admissions are treated as closed. Fee once paid will not be refunded.

Rules made by the Institute regarding selection and closure of admissions shall be final.

7. DOCUMENTS TO BE HANDED OVER AT THE TIME OF JOINING

The Trainee should give an undertaking for good conduct and behavior and to abide by the rules & regulations as prescribed by the Institute.

8. JURISDICTION :

Any legal proceedings instituted for contravening the terms & conditions of the bond is subject to Hyderabad jurisdiction only.

9. GENERAL TERMS AND CONDITIONS FOR TRAINEES ADMITTED TO REGULAR COURSES

- a. No traveling allowance or any other allowance will be paid for attending entrance exam, joining the course or on termination thereof.
- b. The trainees are required to bear all costs including the cost of boarding, lodging, transport and other incidental expenditure when they are taken on Industrial visit to places outside Hyderabad.
- c. Final year trainees are exposed to campus interviews, by indenting Industries. They can appear for interviews and get absorbed. However, the Institute does not guarantee any employment or absorption.

assignments and tests.

- a. Internal assessment of practical exercises.
- b. Examinations in all the subjects.
- c. Evaluation of Project Work and viva-voce by external/ internal examiners for VLSI - EMBEDDED Course.

2.5 Minimum requirements for Award of Certificate

- a. 85% attendance during the programme.
- b. 50% Marks (aggregate)
- c. Minimum 40% marks in each subject.

2.6 Issue of Certificates And Memorandum of Marks:

The Candidates are awarded certificate and memorandum of marks on successful completion of the training.

2.7 Detention/Termination:

The trainees are liable for detention/termination for the following reasons, and if, detained, the training period is liable for extension.

- a. For shortage of attendance (less than 85% to be computed on semester basis)
- b. For non-submitting the exercises/project work
- c. For not securing minimum marks in the examinations.

The trainee is liable for termination, in case, if, he/she violates any of the terms and conditions mentioned in the prospectus, and the course fee paid by him/her will not be refunded.

3. ADMISSION TO LONG TERM COURSES:

Academic batch commences every six months a year. Candidates selected through entrance test are admitted to the long term course.

Use of Unfair Means

Use of unfair means by a candidate at the entrance examination if detected at the time of examination, evaluation or at any other stage will lead to cancellation of his/her examination and debar him/her from appearing in future entrance examinations.

APPLICATIONS:

Prescribed application forms & prospectus can be had from CAD/CAM Department, CITD., Balanagar, Hyderabad-500 037 by demand draft for Rs. 300/- drawn in favour of **Principal Director, CITD., Hyderabad**, on any Nationalized Bank payable at Hyderabad (preferably on State Bank of Hyderabad, Balanagar Branch, Hyderabad-500 037). Request for application received beyond the stipulated date and also receipt of completed applications after the due date will be summarily rejected. If any applicant do not fulfill the requisite qualifications, his/her application is summarily rejected. No correspondence in this matter is entertained.

4. INSTRUCTIONS FOR FILLING UP THE APPLICATION FORMS:

- a. Please read the instructions given in this section carefully before filling the Application Form.
- b. Candidates who have appeared for the qualifying examination and whose results are awaited can also apply by enclosing a bonafide certificate from the concerned Institution / College / University. However, they should qualify at the time of counseling. The Institute is not responsible for any delay in declaration of results of the qualifying examination.
- c. **Photograph:** Affix two recent and identical passport size photographs at the appropriate places attested by a Gazetted Officer.
- d. **Name:** Write your name in capital letters as given in the records of the Board of Secondary Education/University. This name should be given in all future correspondence in the same form. Correct address for correspondence is required to be given.

e. **Date of Birth:** Indicate your date of birth in the boxes provided.

Example: Date of birth is **17 June 1984**

1	7
---	---

0	6
---	---

1	9	8	4
---	---	---	---

Age:

2	5
---	---

f. **Father's /Guardian's Name :** One line

g. **Permanent Address :** Write the complete postal address with PIN CODE to which communication is to be sent.

h. **Academic Qualification :** As reflected under item No.3 of the prospectus.

i. **Attendance Sheet:** Sl. No. 2 & 6 (for office use) The candidate should sign at the appropriate place. Sl. No.3 candidate should write his/her name and address for communication.

j. **Hall Ticket :**Sl. No. 2 (for office use) for POST GRADUATE DIPLOMA IN VLSI – EMBEDDED SYSTEMS. Sl. No.4 candidate should write his/her name and address for communication.

5. SYLLABUS FOR ENTRANCE EXAMINATION:

B E/ B TECH syllabus

6. MODE OF SELECTION:**Selection of Candidates through Entrance Test**

Selection is purely based on merit in the written test conducted by CITD, Hyderabad.

The merit list of the selected candidates will be displayed on the notice board at the Institute and the same will be put on Website **www.citdindia.org**